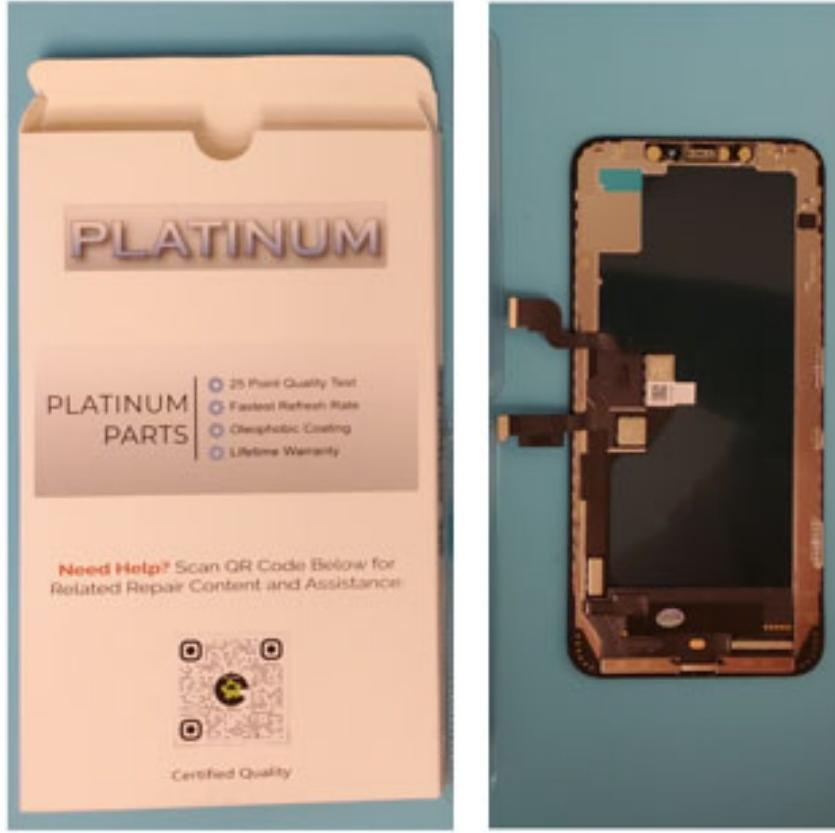
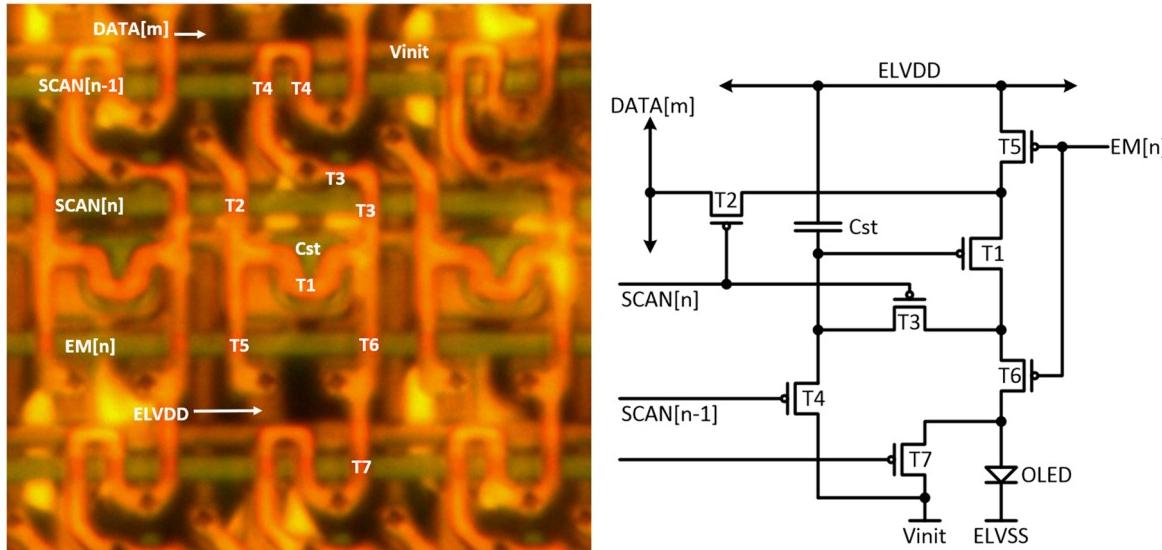
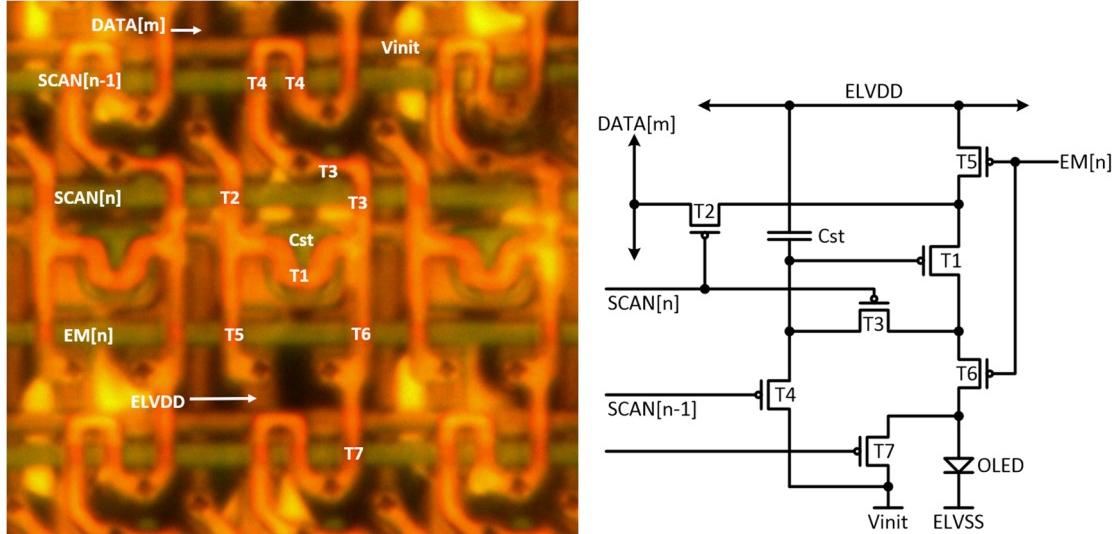
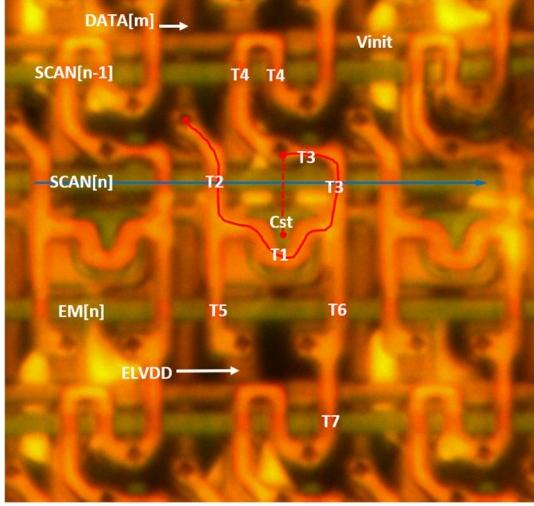
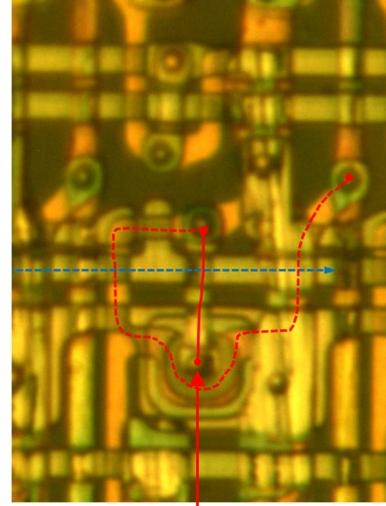


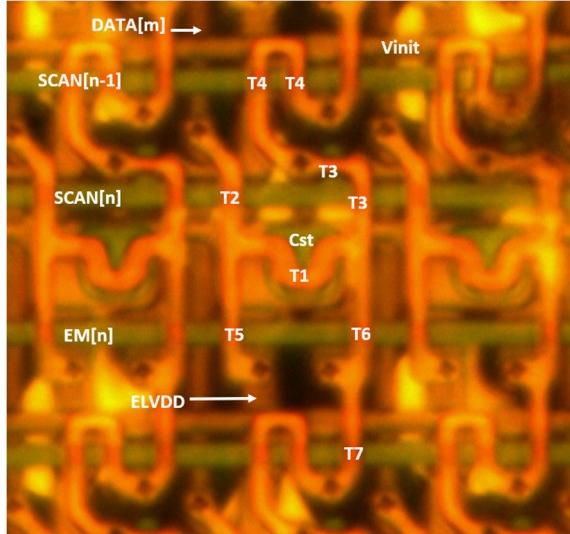
EXHIBIT G

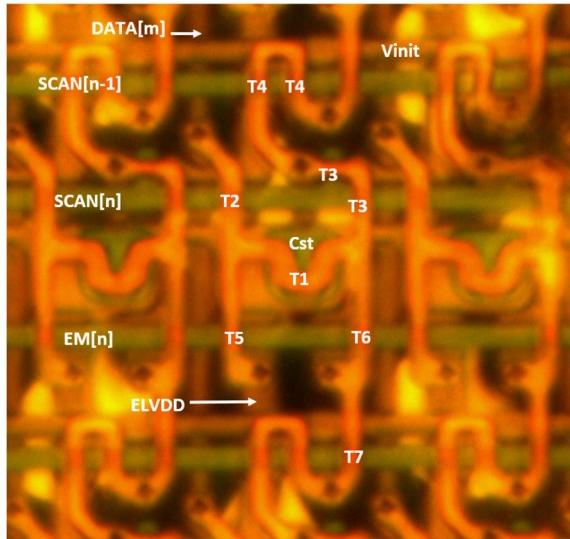
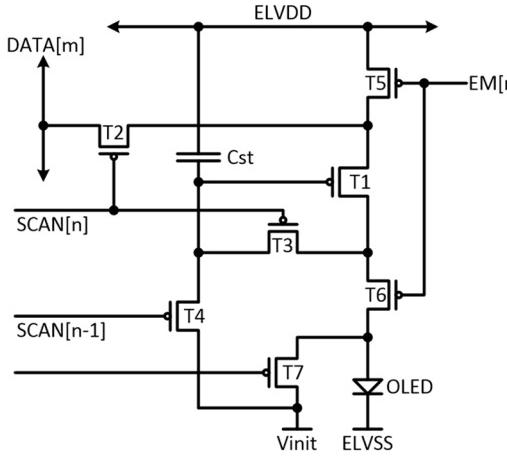
Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
15[pre] A pixel circuit in an organic light emitting device, comprising:	<p>The ETP-822-9401 includes an organic light-emitting diode (“OLED”) display.</p>  <p>The image shows two photographs side-by-side. On the left is the retail packaging for the display, which is a tan-colored card with 'PLATINUM' printed on it. Below that, 'PARTS' is printed next to a vertical line of five small blue circles, each followed by a descriptive word: '25 Point Quality Test', 'Fastest Refresh Rate', 'Oleophobic Coating', and 'Lifetime Warranty'. At the bottom of the card, there is a QR code with the text 'Need Help? Scan QR Code Below for Related Repair Content and Assistance' above it, and 'Certified Quality' below it. On the right is a photograph of the actual OLED display unit, which is a black rectangular panel with internal components visible, resting on a teal surface.</p>

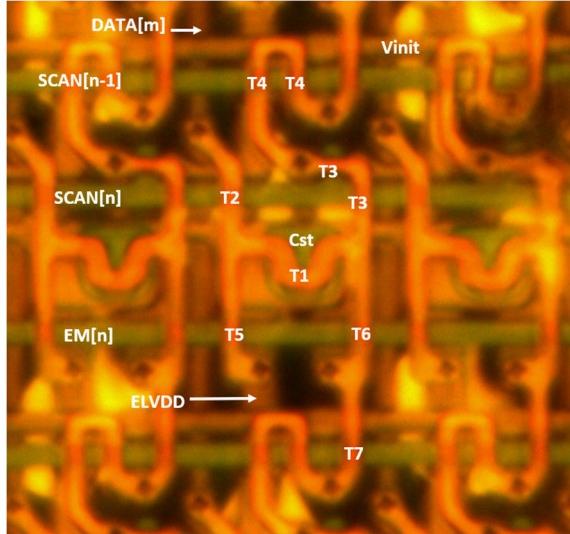
Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
<p>15[pre] A pixel circuit in an organic light emitting device, comprising:</p> <p>(cont'd)</p>	<p>The ETP-822-9401 comprises a pixel circuit in an organic light-emitting device. The annotated backside image (<i>below left</i>) shows a pixel circuit of the eTech Parts 822-9401, including transistors (T1–T7) and a capacitor (Cst). An exemplary circuit diagram of the pixel circuit of the eTech Parts 822-9401 is also shown (<i>below right</i>).</p>  <p>Annotated Backside Image Labels:</p> <ul style="list-style-type: none"> DATA[m] SCAN[n-1] SCAN[n] EM[n] ELVDD T4 T4 T2 T3 T3 Cst T1 T5 T6 T7 Vinit ELVSS OLED <p>Circuit Diagram Labels:</p> <ul style="list-style-type: none"> DATA[m] ELVDD EM[n] T5 T2 Cst T1 SCAN[n] T3 SCAN[n-1] T4 T7 Vinit ELVSS OLED

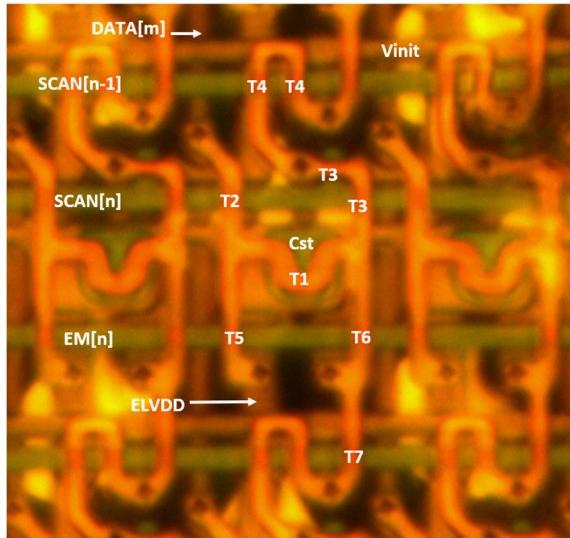
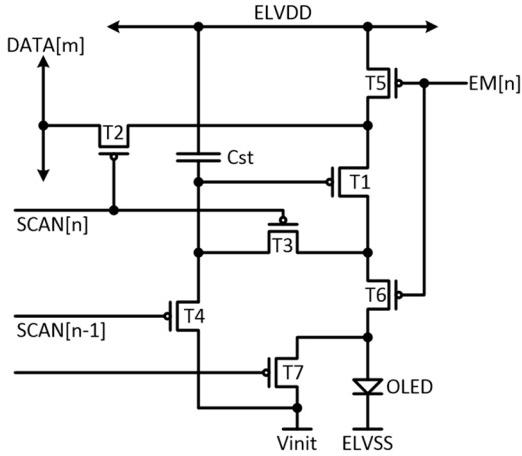
Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
15[a] a first transistor including a gate to which a current scan signal is applied, and a source to which a data signal voltage is applied;	<p>The pixel circuit of the ETP-822-9401 has a first transistor including a gate to which a current scan signal is applied, and a source to which a data signal voltage is applied. As shown in the annotated backside image below, a source of the first transistor T2 is coupled to a data line DATA[m] for delivering a data signal voltage. The gate of first transistor T2 is coupled to a scan line SCAN[n] that provides a current scan line signal. In response to an active (low) current scan line signal delivered on scan line SCAN[n], first transistor T2 delivers a data signal voltage from data line DATA[m].</p>  <pre> graph TD T2[Transistor T2] --- DATAm[DATA[m]] T2 --- SCANn[SCAN[n]] T3[Transistor T3] --- Cst[Cst] T3 --- T1[Transistor T1] T4[Transistor T4] --- SCANn1[SCAN[n-1]] T4 --- T7[Transistor T7] T5[Transistor T5] --- Vinit[Vinit] T5 --- T6[Transistor T6] T6 --- T7 T7 --- OLED[OLED] T7 --- ELVSS[ELVSS] T1 --- EMn[EM[n]] T1 --- T5 T6 --- T5 T6 --- T7 T7 --- Vinit T7 --- ELVSS </pre>

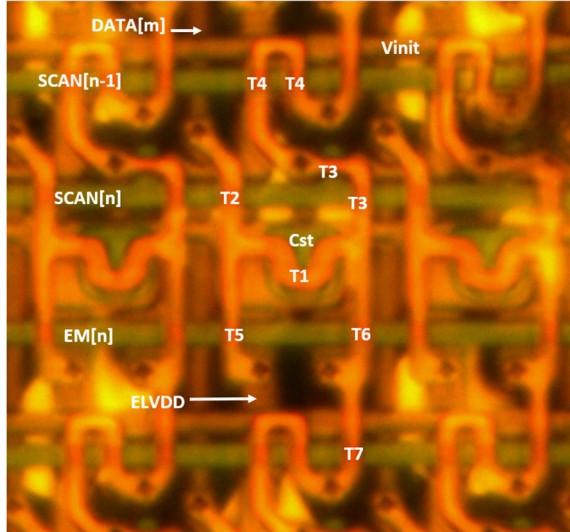
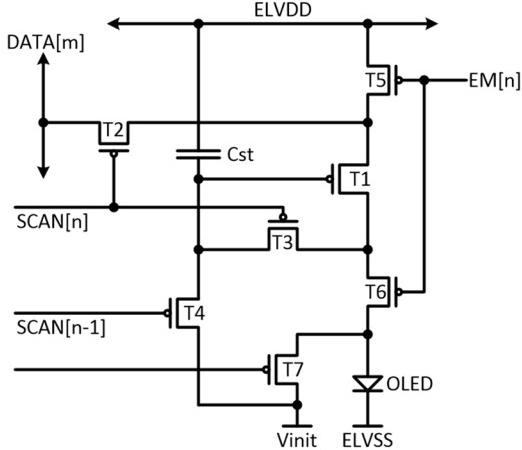
Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
<p>15[a] a first transistor including a gate to which a current scan signal is applied, and a source to which a data signal voltage is applied;</p> <p>(cont'd)</p>	<p>For example, as shown in the exemplary annotated images below, in response to an active (low) current scan line signal (blue line) delivered on scan line SCAN[n], first transistor T2 delivers the data signal voltage (red line) present on data line DATA[m]. The annotated images below depict an exemplary voltage path from data line DATA[m] through transistors T2-T1-T3 to the gate of transistor T1.</p> <div style="display: flex; justify-content: space-around;"> <div style="text-align: center;"> <p>Delivery of Data Signal Voltage (backside image)</p>  </div> <div style="text-align: center;"> <p>Delivery of Data Signal Voltage (front-side image)</p>  <p>Contact Hole (to T1 Gate Electrode)</p> </div> </div>

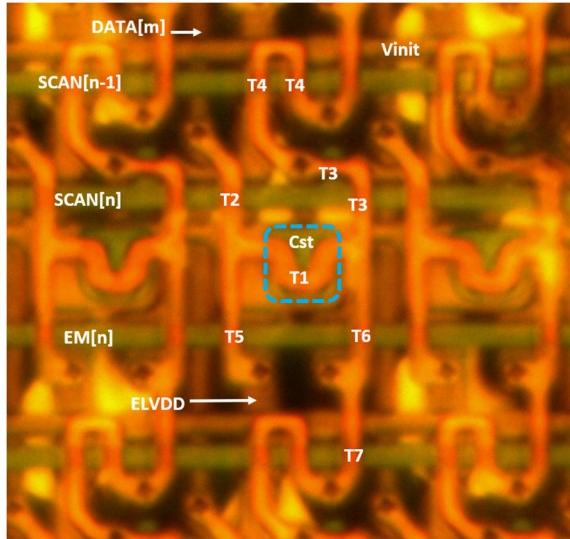
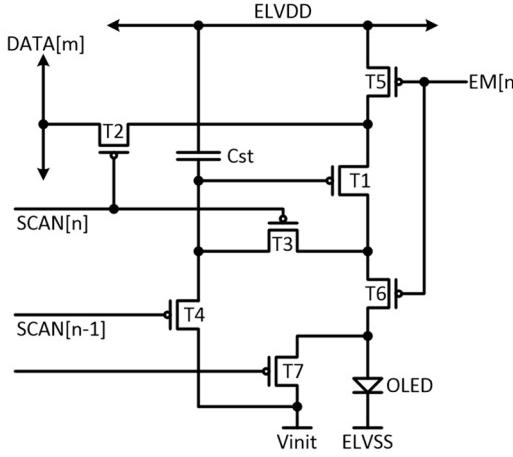
Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
15[b] a second transistor whose source is coupled to a drain of the first transistor;	<p>The pixel circuit of the ETP-822-9401 has a second transistor whose source is coupled to a drain of the first transistor. As shown in the annotated backside image below, the source of the second transistor T1 is coupled to the drain of first transistor T2.</p>  <pre> graph TD ELVDD --> T2 T2 --- T3 T3 --- Cst Cst --- T1 T1 --- T5 T5 --- EMn[EM[n]] EMn --- T6 T6 --- T7 T7 --- Vinit[Vinit] T7 --- T4 T4 --- SCANn1[SCAN[n-1]] T4 --- SCANn[SCAN[n]] T4 --- T6 T6 --- T7 T7 --- OLED T7 --- ELVSS[ELVSS] </pre>

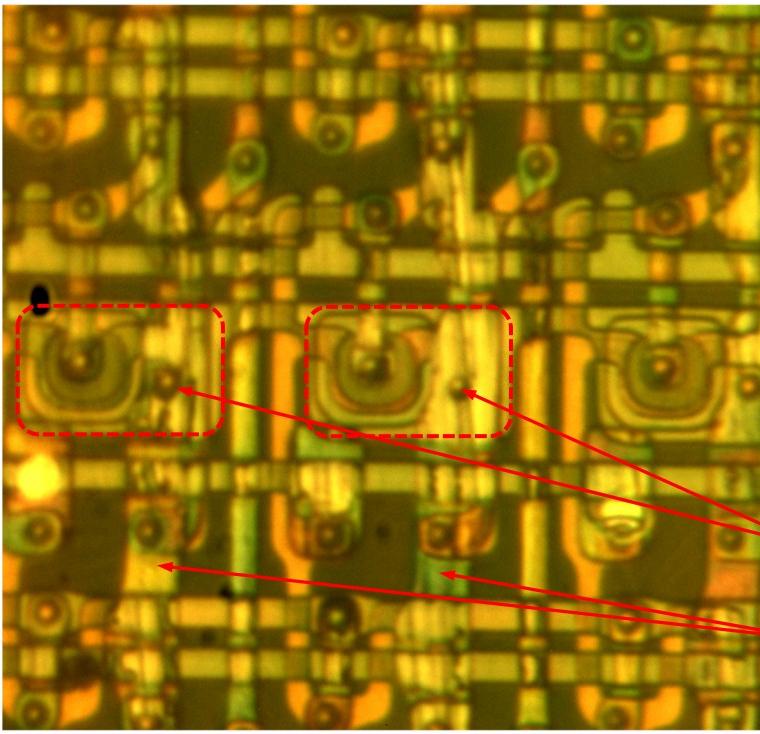
Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
15[c] a third transistor whose drain and source are connected between a gate and a drain of the second transistor;	<p>The pixel circuit of the ETP-822-9401 has a third transistor whose drain and source are connected between a gate and a drain of the second transistor. As shown in the annotated backside image below, a third transistor T3 has drain and source electrodes connected between gate and drain electrodes of the second transistor T1, respectively.</p> <div style="display: flex; justify-content: space-between;"> <div style="flex: 1;">  </div> <div style="flex: 1;">  </div> </div>

Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
15[d] a fourth transistor including a gate to which a current light-emitting signal is applied, a source to which a power supply voltage is applied, and a drain coupled to the source of the second transistor;	<p>The pixel circuit of the ETP-822-9401 has a fourth transistor including a gate to which a current light-emitting signal is applied, a source to which a power supply voltage is applied, and a drain coupled to the source of the second transistor. In the annotated backside image below, a fourth transistor T5 has a source coupled to ELVDD and a drain coupled to the source of transistor T1. In response to an active (low) current light-emitting signal delivered on emission line EM[n], fourth transistor T5 delivers a power supply voltage from ELVDD to second transistor T1.</p>  <pre> graph LR DATA[DATA[m]] --> T2 T2 --- T2_Gate[] T2_Gate --- SCANn[SCAN[n]] T2 --- Cst_Cond[] Cst_Cond --- Cst[Capacitor Cst] Cst --- T1_Gate[] T1_Gate --- T1_Drain[] T1_Drain --- T1_Source[] T1_Source --- T5_Drain[] T5_Drain --- T5_Gate[] T5_Gate --- EMn[EM[n]] T5_Gate --- ELVDD[ELVDD] T5_Gate --- T5_Drain[] T5_Drain --- T6_Drain[] T6_Drain --- T6_Gate[] T6_Gate --- T6_Drain[] T6_Drain --- T7_Drain[] T7_Drain --- T7_Gate[] T7_Gate --- Vinit[Vinit] T7_Gate --- ELVSS[ELVSS] T7_Gate --- T7_Drain[] T7_Drain --- OLED_OLED[OLED] </pre>

Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
<p>15[e] a fifth transistor including a gate to which the current light-emitting signal is applied, a source coupled to the drain of the second transistor, and a drain coupled to one terminal of an electroluminescent element;</p>	<p>The pixel circuit of the ETP-822-9401 has a fifth transistor including a gate to which the current light-emitting signal is applied, a source coupled to the drain of the second transistor, and a drain coupled to one terminal of an electroluminescent element. In the annotated backside image below, a fifth transistor T6 has a source coupled to a drain of second transistor T1, and a drain is coupled to one terminal of an electroluminescent element (OLED) through a contact hole. As further shown in the annotated image below, in response to an active (low) current light-emitting signal delivered on emission line EM[n], fifth transistor T6 turns on and delivers driving current that flows through second transistor T1 to the OLED.</p> <div style="display: flex; justify-content: space-around;">   </div>

Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
<p>15[f] the electroluminescent element having the one terminal coupled to the drain of the fifth transistor and the other terminal grounded; and</p>	<p>The pixel circuit of the ETP-822-9401 has an electroluminescent element with one terminal coupled to the drain of the fifth transistor and the other terminal grounded.</p> <p>As shown in the annotated backside image below, the pixel circuit has an electroluminescent element (OLED) with a first terminal, the anode, coupled to the drain of the fifth transistor T6. The other terminal of the OLED is grounded to facilitate the flow of current from fifth transistor T6 through the anode of the OLED to the cathode in the light-emission phase.</p>  

Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
<p>15[g] a capacitor in which one terminal of the capacitor is coupled to the gate of the second transistor and a power supply voltage is applied to the other terminal of the capacitor.</p>	<p>The pixel circuit of the ETP-822-9401 has a capacitor in which one terminal of the capacitor is coupled to the gate of the second transistor and a power supply voltage is applied to the other terminal of the capacitor.</p> <p>As shown in the annotated backside image below, blue dashes outline the lower plate of the capacitor Cst, which is coupled to the gate of the second transistor T1.</p>  

Claim 15	eTech Parts Plus 822-9401 OLED Display (“ETP-822-9401”)
<p>15[g] a capacitor in which one terminal of the capacitor is coupled to the gate of the second transistor and a power supply voltage is applied to the other terminal of the capacitor.</p> <p>(cont'd)</p>	<p>As shown in the annotated front-side image below, red dashes outline the upper plate of the capacitor Cst that is coupled to the power supply voltage ELVDD.</p>  <p>The image shows a close-up of an OLED display's internal circuitry. Two specific capacitors are highlighted with red dashed boxes. Red arrows point from these boxes to labels: 'Contact Holes (to ELVDD)' and 'Power Supply Voltage (ELVDD)'. The labels indicate that the top plate of the capacitor is connected to the power supply voltage ELVDD through contact holes.</p>